

## IN THE SPECIFICATION:

Following the Title of the application, please add the following section heading and paragraph:

### --CROSS REFERENCE TO RELATED APPLICATIONS

This application is a continuation of application Serial No. 09/580,204 filed May 26, 2000, now United States Patent No. 6,647,418, which is a continuation of application Serial No. 08/956,993 filed October 23, 1997, now United States Patent No. 6,092,110, which are all incorporated herein in their entireties by reference thereto.—

Please re-write the second full paragraph bridging pages 3 and 4 as follows:

From the foregoing, it can be appreciated that a substantial ~~needs~~need exists for a high performance data packet filter which can work with a large number of source IP addresses. There is also a need for an efficient way to administer source IP address lists.

Please re-write the fourth and fifth full paragraphs starting on page 7 and bridging onto page 8 as follows:

The heart of packet filter processor ~~38-14~~ is a dedicated high performance microprocessor 38. Any microprocessor capable of operating at the speeds necessary to implement of the functions of the packet filter processor is appropriate. Examples of processors suitable to practice the invention includes the INTEL family of processors, such as the Pentium®, Pentium® Pro, and Pentium® II microprocessors.

Packet filter processor 14 also includes a connector 34 and interface 36, both of which are attached to ~~processor~~microprocessor 38. Connector 34 and interface 36 both adhere to Electronic Industries Association (EIA) Standard RS-232-C titled "Interface Between Data Terminal Equipment and Data Communication Equipment Employing Serial Binary Data Interexchange," October, 1969. Finally, packet filter processor 14 includes a clock 26 and clock counter 28 to control the timing of packet filter processor 14.